

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





SOT-23 Formed SMD Package

CMBT2222 CMBT2222A

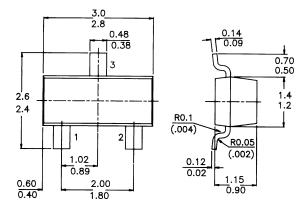
SILICON PLANAR EPITAXIAL TRANSISTORS

N-P-N silicon transistors

Marking CMBT2222 = lB

CMBT2222A = IP

PACKAGE OUTLINE DETAILS
ALL DIMENSIONS IN mm



CMPTOOOD CMPTOOODA

Pin configuration

1 = BASE 2 = EMITTER

3 = COLLECTOR



ABSOLUTE MAXIMUM RATINGS

		CA	ABTZZZZ	CMBTZZZZ	\boldsymbol{A}
Collector-base voltage (open ernitter)	V_{CB0}	max.	60	75	\overline{V}
Collector-emitter voltage (open base)	V_{CE0}	max.	30	40	V
Emitter base voltage (open collector)	V_{EB0}	max.	5,0	6,0	V
Collector current (d.c.)	I_C	max.	600		mA
Total power dissipation up to $T_{amb} = 25$ °C	P_{tot}	max.	250		mW
D.C. current gain					
$I_C = 150 \text{mA}; \ V_{CE} = 10 V$	h_{FE}	100 to 300			
$IC = 500mA; V_{CE} = 10V$	h_{FE}	>	30	40	
Transition frequency at $f = 100 \text{ MHz}$					
$I_C = 20 \text{ mA}; \ V_{CE} = 20 \ V$	f_T	>	250	300	MHz

CMBT2222 CMBT2222A

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified) Limiting values

		CA	<i>MBT2222</i>	CMBT2222	^{2}A
Collector-base voltage (open emitter)	V_{CBO}	max.	60	75	\overline{V}
Collector-emitter voltage (open base)	V_{CEO}	rnax.	30	40	V
Emitter-base voltage (open collector)	V_{EBO}	max.	5,0	6,0	V
Collector current (d.c,)	I_C	max.	600		mA
Total power dissipation up to $T_{amb} = 25$ °C	P_{tot}	max.	250		mW
Storage temperature range	T_{Stg}		-55 to	$^{\circ}$ C	
Junction temperature	T_j	max.	1	50	$^{\circ}$ C

THERMAL RESISTANCE

From junction to ambient $R_{th\ j-a}$ 500 K/W

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified

-			CMBT2222	CMBT2222A	
Collector cut-off current					-
$I_E = 0; \ V_{CB} = 50 \ V$	I_{CBO}	<	0,01		μΑ
$I_E = 0; \ V_{CB} = 60 \ V$	I_{CBO}	<	-	0,01	μA
I_E = 0; V_{CB} = 50 V; T_i - 125 °C	I_{CBO}	<	10	_	μA
$I_E = 0$; $V_{CB} = 60 \text{ V}$; $T_j = 125 ^{\circ}\text{C}$	I_{CBO}	<	-	10	μA
$V_{EB} = 3 \ V; \ V_{CE} = 60 \ V$	I_{CEX}	<-	_	10	nA
Base current					
with reverse biased emitter junction					
$V_{FB} = 3V$; $V_{CE} = 60V$	I_{BEX}	<	-	20	nA
Emitter cut-off current					
$I_C = 0$; $V_{EB} = 3V$	I_{EBO}	<	-	10	nΑ
Saturation voltages					
$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}$	V_{CEsat}	<	400	300	mV
	V_{BEsat}	<	1.3	_	V
	V_{BEsat}		-	0,6 to 1,2	V
$I_C = 500 \text{ mA}; \ I_B = 50 \text{ mA}$	V _{CEsat}	<	1.6	1.0	V
	V_{BEsat}	<	2.6	2.0	V
Breakdown voltages					
$I_C = 1.0 \mu A; I_B = 0$	$V_{(BR)C}$	EO >	30	40	V
$I_C = 100 \mu A; I_E = 0$	$V_{(BR)C}$	BO >	60	75	V
$I_C = 0$; $I_E = 10 \mu A$	$V_{(BR)EI}$			6,0	V

CMBT2222 CMBT2222A

			CMBT2222	CMBT222	2A
D.C. current gain					
$I_C = 0.1 \text{ mA}; V_{CE} = 10V$	h_{FE}	>	3	25	
$I_C = 1 \text{ mA}; V_{CE} = 10V$	h_{FE}	>	5	70	
$l_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}$	h_{FE}	>	7	' 5	
$l_C = 10 \text{ mA}; V_{CE} = 10 \text{ V}; T_{amb} = -55 \text{ °C}$	h_{FE}	>	3	25	
$I_C = 150 \text{mA}; \ V_{CE} = 10 V$	h_{FE}		100 to 300		
$I_C = 150 \text{ mA}; V_{CE} = 1 \text{ V}$	h_{FE}	>	50		
$I_C = 500 \text{ mA}; V_{CE} = 10 \text{ V}$	h _{FE}	>	30	40	
Transition frequency at $f = 100 \text{ MHz}$					
$I_C = 20 \text{ mA}; V_{CE} = 20 \text{ V}$	f_T	>	250	300	MHz
Output capacitance at $f = 1$ MHz					
$I_E = 0; \ V_{CB} = 10V$	Co	<	8,	,0	pF
Input capacitance at $f = 1$ MHz					
$I_C = 0; \ V_{EB} = 0.5V$	Ci	<	30	25	рF
Noise figure at $R_S = 1 k\Omega$					
$I_C = 100 \mu A; V_{CE} = 10V; f = 1 \text{ kHz}$	F	<	4,	,0	dB
Switching times (between 10% and 90% levels)					
Turn-on time switched to $I_c = 150 \text{ mA}$					
delay time	t_d	<	1	0	ns
rise time	t_{Γ}	<	2	25	ns
Turn-off time switched from $I_c = 150 \text{ mA}$					
storage time	t_S	<	225		ns
fall time	t_f	<	6	20	ns
Small Signal Current Gain					
$V_{CE} = 10V; I_C = 1 \text{ mA}; f = 1 \text{ KHz}$	h_{fe}	>	5	70	
		<	30	00	
$V_{CE} = 10V; I_C = 10mA; f = 1 \text{ KHz}$	h_{fe}	>	7	75	
		<	3	75	

Customer Notes

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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